

IST8306

3D Magnetometer

Datasheet

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1. General Description

iSentek IST8306 is a 3-axis digital magnetometer with $0.8 \times 0.8 \times 0.53 \text{ mm}^3$, 4-pin WLCSP-BGA package. It is an integrated chip with 3-axis magnetic sensors, digital control logic, a built-in temperature compensation circuit and self-test function. IST8306 provides an I²C digital output with fast mode up to 400 kHz. The high output data rate, ultra-low noise, ultra-low hysteresis and excellent output stability over working temperature range make it a perfect product for high accuracy applications.

Features

- Monolithic 3-axis magnetic sensor
- Ultra-compact package size of $0.8 \times 0.8 \times 0.53 \text{ mm}^3$, 4-pin WLCSP-BGA package
- I²C slave, Fast Mode up to 400 kHz
- Wide dynamic range of $\pm 3000 \mu\text{T}$ for each axis
- Maximum 200 Hz output data rate
- Ultra-low hysteresis (0.1 %FS)
- Low suspend current of $0.5 \mu\text{A}$
- Wide operating temperature range
- High precision temperature compensation
- Built-in self-test function
- Built-in noise suppression filter
- Software and algorithm support are available (for tilt compensation and soft/hard iron calibration)
- RoHS, HF and TSCA compliant

Applications

- Quadcopter/Drone Applications
- Augmented Reality Applications
- Virtual Reality Applications
- Location Based Services
- Navigation Applications
- Industrial Applications
- Magnetometry
- IoT devices
- Heading
- Gaming

2. Block Diagram, Package Dimension and Application Circuit

2.1. Block Diagram

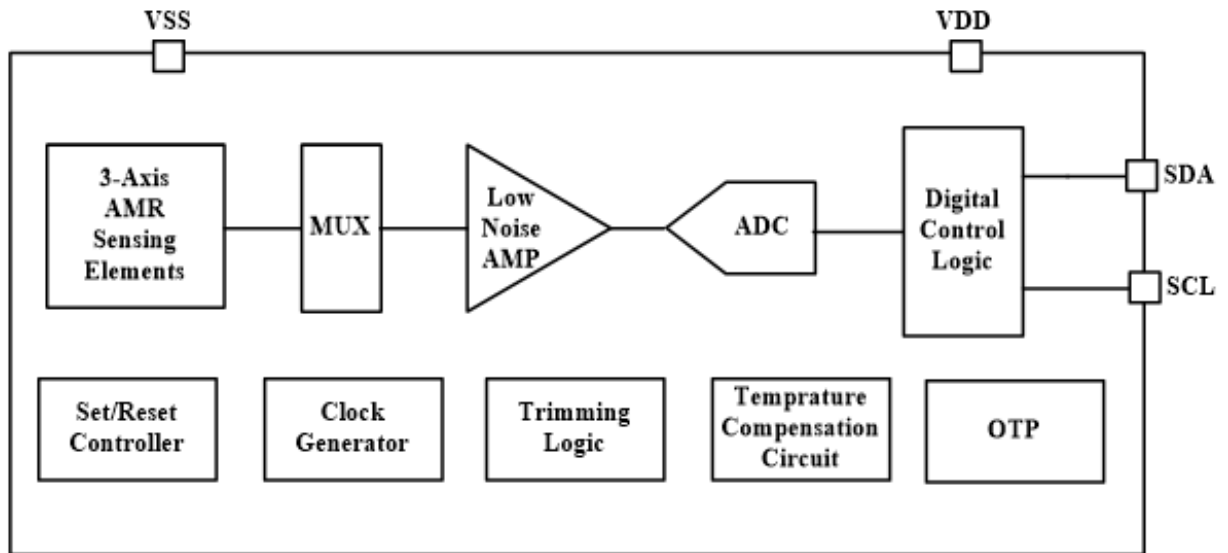


Figure 1. Block Diagram

2.2. Package Dimensions and Magnetic Field Directions

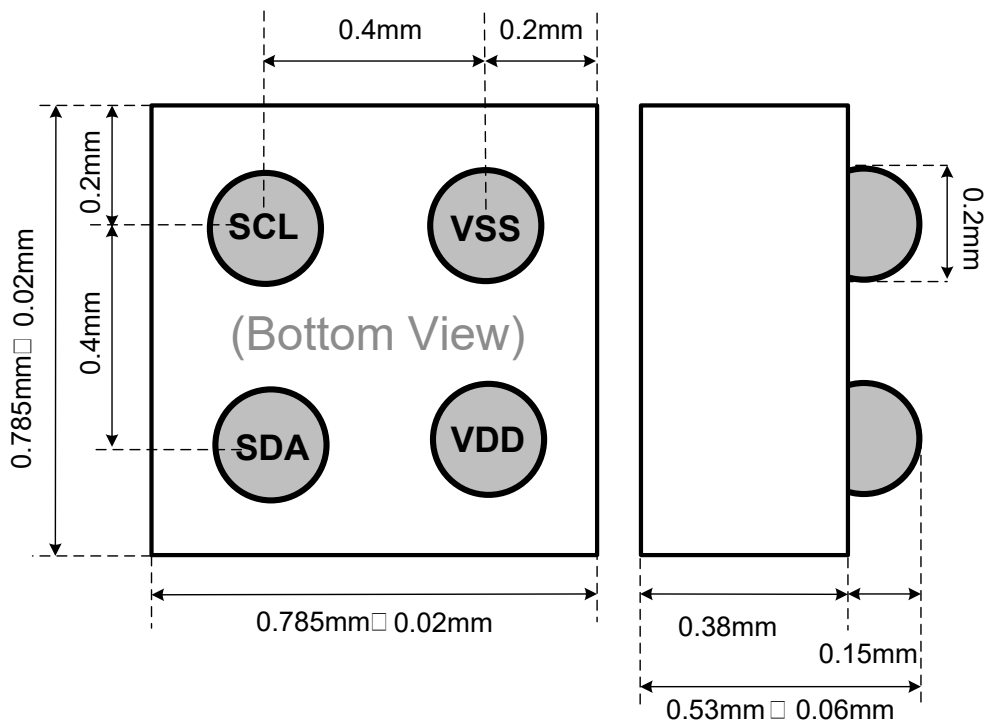


Figure 2. Package Dimensions

IST8306 3D TOP View

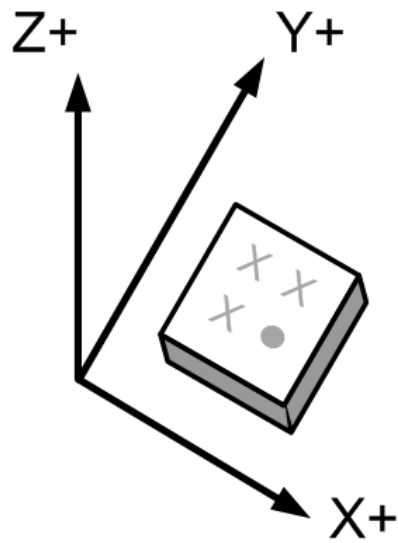
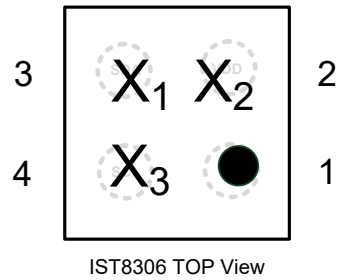


Figure 3. Magnetic Field Directions

2.3. Marking Information

Date code X_1X_2
 Product code $X_3 \bullet$

X_1 : Year
 X_2 : Week
 X_3 : Product code



IST8306 TOP View

2.4. Pin Configurations and Functions

Pin No	Pin Name	I/O type	Function
1	VSS	Supply	Ground
2	VDD	Supply	Power supply voltage, 1.65V to 1.95 V
3	SDA	I/O	I ² C data
4	SCL	I	I ² C clock

2.5. Application Circuit

The application circuits for the IST8306 sensor are depicted in *Figure 4* and *Figure 5*, showing standard and alternative configurations to accommodate different space constraints.

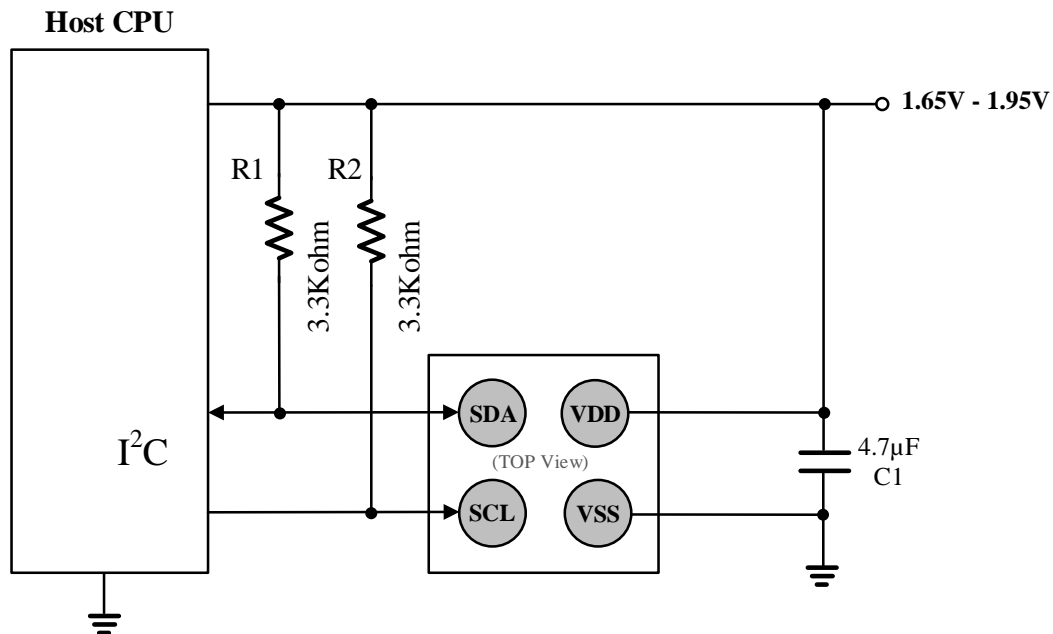


Figure 4. Application Circuit

Note: This setup is ideal for applications with sufficient space to accommodate the 4.7 μF capacitor.

For applications where space is limited, the application circuit can be modified as follows:

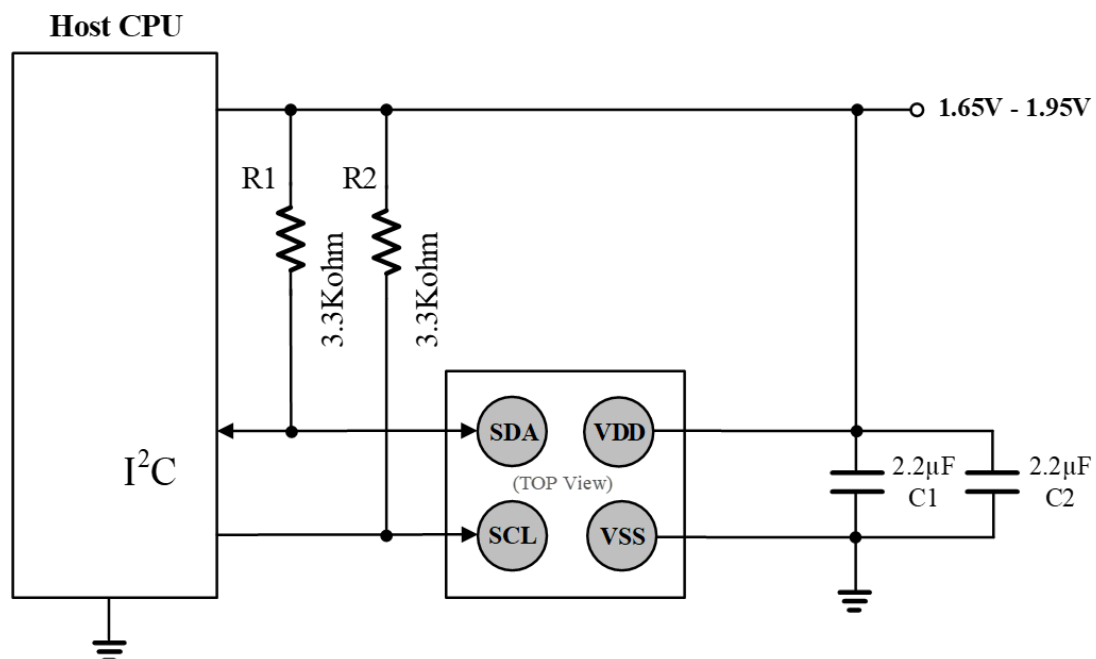


Figure 5. Alternative Application Circuit

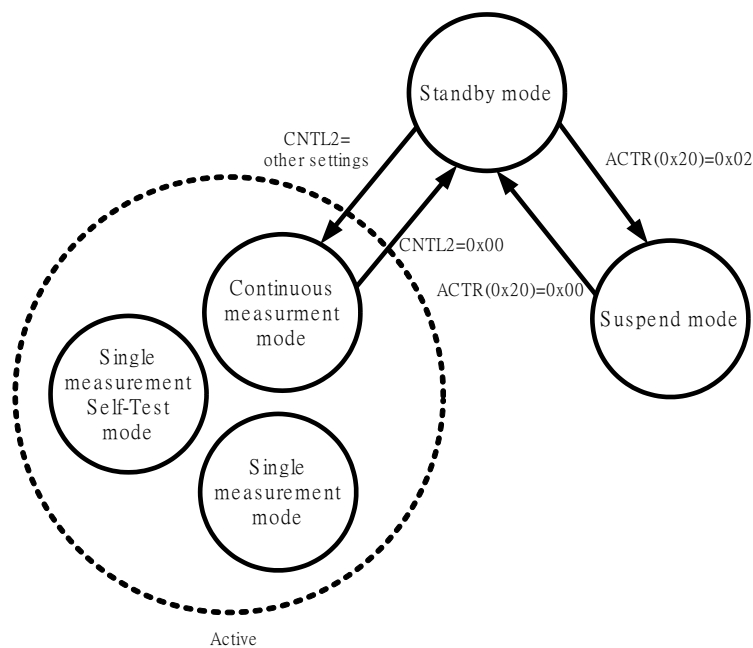
Note: Two 2.2 μF capacitors (size 0201) in parallel replace the single 4.7 μF capacitor.

3. Operational Modes and Functional Descriptions

3.1. Operation Modes

IST8306 has following operation modes:

- (1) Standby Mode
- (2) Suspend Mode
- (3) Single Measurement Mode
- (4) Continuous Measurement Mode
- (5) Single Measurement Self-test Mode



3.1.1. Stand-By Mode

The initial mode (after power on) of IST8306 is Standby Mode. In Standby Mode, all internal circuits are off (except oscillator and regulator), and all registers are accessible. The latest state of data saved in Read/Write registers is maintained. Registers can be reset by soft reset.

3.1.2. Suspend Mode

When Suspend Mode is set through ACTR(0x20), all circuits except I²C related will be turned off. This is a mode with extremely low power consumption (1 μ A). Users are advised to use this mode when the system does not require e-compass data. Users must first enter Standby Mode before using additional modes.

3.1.3. Single Measurement Mode

In Single Measurement Mode, the measured data is stored in data registers then IST8306 automatically transitions to Standby Mode. When entering Standby Mode, Control Register 1 (CNTL2[3:0]) turns to "0000". Simultaneously, DRDY bit in STAT1 register turns to "1". This is called "data ready". When any of the measurement data registers or STAT1 register is read, DRDY bit turns to "0". For the next measurement, user must set Control Register 1 (CNTL2[3:0]) to "0001" again.

3.1.4. Continuous Measurement Mode

When Continuous Measurement Mode is set, sensor is measured periodically at preset frequencies. The measured data is stored in Output Data Registers. When the next measurement time comes, IST8306 automatically resumes measuring again and the Output Data Registers will be updated.

3.1.5. Single Measurement Self-Test Mode

Self-Test mode is used to ensure that the IC is working correctly. One time measurement in Self-Test Mode can be done by setting Self-Test Register (STR[6:0]) to "1000000". User can compare this measurement result with other measurement modes' result when the magnetic environment remains the same. If all 3-axis outputs' polarities are different, the IC is working correctly.

3.2. DRDY Function

DRDY function is used when the output data is updated. The DRDY register is used to monitor the data ready output. After reading data from the output register (assume DRP bit is "1", as default setting), DRDY I becomes low.

3.3. IST8306 Read Process

(1) Read STAT register:

- Polling STAT register bit 0
- DRDY: shows if the data is ready or not
 - 0: no data ready
 - 1: data ready
- DOR: shows if any data has been skipped before the current data
 - 0: no skipped data

1: data skipped.

(2) Read Measurement Data:

Read Register 0x11h ~ 0x16h for X, Y and Z axis data. When data reading starts, DRDY bit and DOR bit turn to "0".

4. Electrical Specifications

4.1. Absolute Maximum Ratings

Parameter	Symbol	Limits	Unit
Storage Temperature	TSTG	-40 to 125	°C
Supply Voltage	VDD	-0.3 to 1.98	V
Electrostatic Discharge Voltage*1	VESD_HBM	-4000 to 4000	V
Electrostatic Discharge Voltage*2	VESD_MM	-400 to 400	V
Electrostatic Discharge Voltage*3	VESD_CDM	-800 to 800	V
Reflow Classification	JESD22-A113 with 260 °C Peak Temperature		

1. Human Body Model (HBM)
2. Machine Model (MM)
3. Charge Device Model (CDM)

4.2. Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
Operating Temperature	TA	-20		+85	°C
Supply Voltage	VDD	1.65	1.8	1.95	V

4.3. Electrical Specifications

(Operating conditions: TA = +25 °C; VDD = 1.8 V; 4.7 μF ceramic capacitors tied to between VDD & VSS pin with maximum allowed line width and 5mm distance.)

Parameter	Symbol	Conditions	Min.	Typ.	Max	Unit
Operating Current	IDD3A	with OSR* ¹ =32 setting, 10 sps 50 sps 100 sps		0.25 1.2 2.3		mA
Suspend Current	ISPD			0.5		uA
Standby Mode Current	ISBM			8		uA
Output Data Rate (ODR)	ODR			200* ²		Hz
Input Low Voltage	VIL		0		VDD *30%	V
Input High Voltage	VIH		VDD *70%		VDD	V
Output Low Voltage	VOL	IOL = +4 mA	0		VDD *20%	V
Output High Voltage	VOH	IOH = -100 uA (Except SCL and SDA)	VDD *80%		VDD	V

1. Register OSRCNTL(0x41) controls OSR setting.

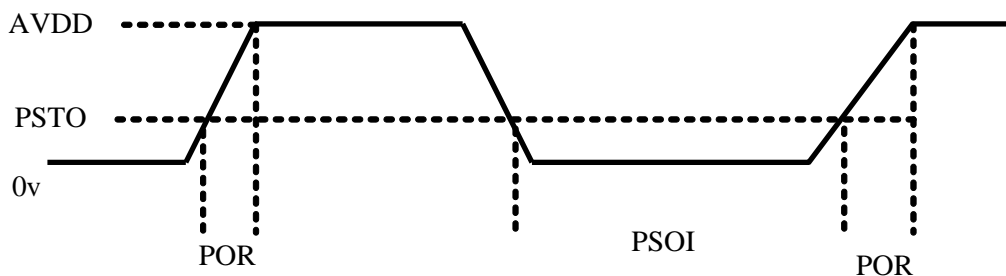
2. 200 Hz ODR can be achieved with OSR ≤ 16.

4.4. Magnetic Sensor Specifications

(Operating conditions: TA = +25°C; VDD = 1.8 V; 4.7 μF ceramic capacitors tied to between VDD & VSS pin with maximum allowed line width and 5mm distance.)

Parameter	Condition	Min.	Typ.	Max	Unit
Dynamic Range (Each Axis)	Total Applied Field		±3000		uT
Linearity	±200 uT Range		0.1		%FS
	±3000 uT Range		1.5		
Resolution			0.3		uT/LSB
Sensitivity			3.3		LSB/uT
Hysteresis			0.1		%FS
RMS Noise			0.8		uT

4.5. Power On Reset (POR) Specifications



PSTO: Power Supply Turn Off voltage
 PSOI: Power Supply Turn Off Interval
 POR: Power On Reset

PSTO: max=0.7volt
 PSOI: min=10ms
 POR: max:50ms

When POR circuit detects the rise of VDD voltage, it resets all internal circuits and initializes all registers. After reset, IST8306 transits to Standby mode.

5. Technology Overview

5.1. AMR Technology

IST8306, an iSentek patented magnetometer is designed based on Anisotropy Magnetoresistance (AMR) technology. The output is generated from the resistance change of the AMR resistors as external magnetic field varies. The sensitivity is approximately 50 to 200 times greater than conventional Hall elements. The high sensitivity allows a higher output data rate (ODR), lower noise, and lower power consumption.

5.2. High Reliability Monolithic Design

IST8306 consists of full AMR Wheatstone Bridges detecting magnetic components in three orthogonal directions. The monolithic packaging of IST8306 enables outstanding stability to thermal shock and ultra-small footprint, making it a highly efficient and robust device.

5.3. Ultra-low Hysteresis Design

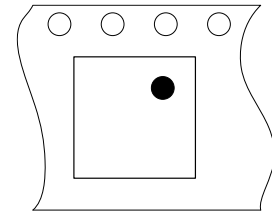
The ultra-low hysteresis design of IST8306 prevents the magnetometer from experiencing dynamic offset after encountering a strong external magnetic field impact. Practically, the angular accuracy is restored automatically after the removal of magnetic interference. This feature of IST8306 minimizes the requirement for user calibration, providing a much better user experience as compared to conventional products like Hall sensors, which need frequent calibration to ensure required precision.

5.4. Magnetic Setting Mechanism

AMR sensing resistors consist of high permalloy thin film and metallization. Because AMR resistors are made of soft magnetic material, irreversible magnetic rotation may occur after experiencing a strong external magnetic field, resulting in angular error induced by the residual magnetization in the sensitive direction. To solve this issue, a magnetic setting mechanism was introduced in IST8306. A magnetic field is generated within IST8306 to align the magnetization of AMR sensing resistors before every measurement. This auto-zeroing mechanism ensures the stability of the IST8306's angular accuracy throughout the operation.

6. Packing Information

Reel tape with round hole facing up, Pin 1 positioned at the top right.
 Moisture Sensitivity Level (MSL): 1



7. Ordering Information

Order Number	Package Type	Packaging	Marking Information
IST8306	WLCSP – 4 pin	Tape and Reel: 5k pieces per reel	X_1X_2 X_3 ● X ₁ : Last number of the year X ₂ : Week number X ₃ : Product code

For more information on iSentek’s magnetic sensors, please send an email to sales@isentek.com or visit our website at www.isentek.com.

The sensing technology of IST8306 is protected by US Patent 9,970,997, CN Patent CN106125020A.

8. Legal Disclaimer

8.1. Warranty and Liability Disclaimer

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8.3. Disclaimer Regarding Changes

iSentek reserves the right to modify the contents of this datasheet, including specifications and descriptions, at any time and without prior notice. This document supersedes all previously issued information.

Revision History

Revision Version	Date	Description
1.2	March 2 nd , 2021	Initial release
1.3	October 14 th , 2021	Added explanations for registers 0x35, 0x40, and 0x43; corrected error in the package diagram.
1.4	May 16 th , 2024	Edited application circuit note; added details and the application circuit for case with 2 capacitors (Page 8).
1.5	October 29 th , 2024	Added packing information (Page 26); added Moisture Sensitivity Level (MSL): 1 (Page 26)